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FIG. 1

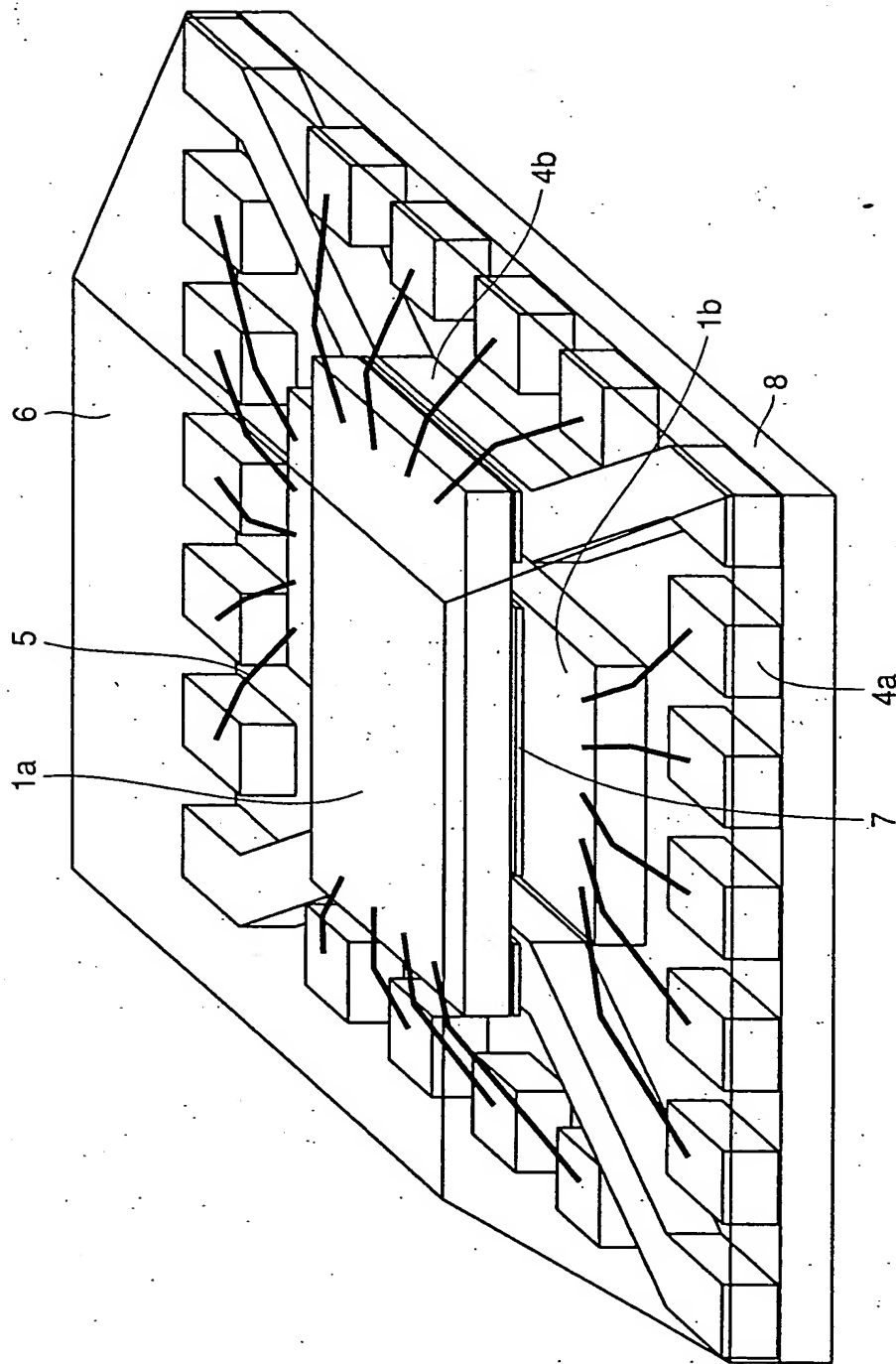


FIG. 2

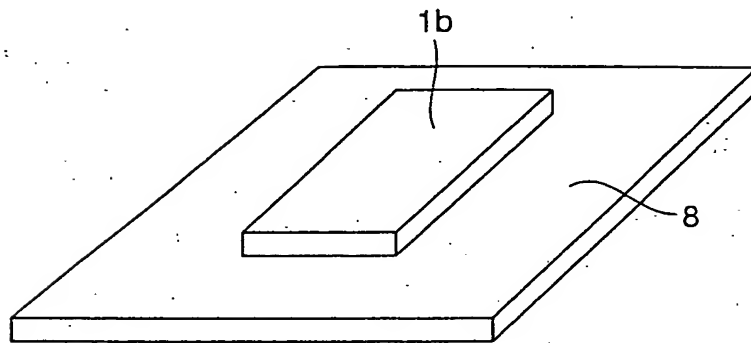


FIG. 3

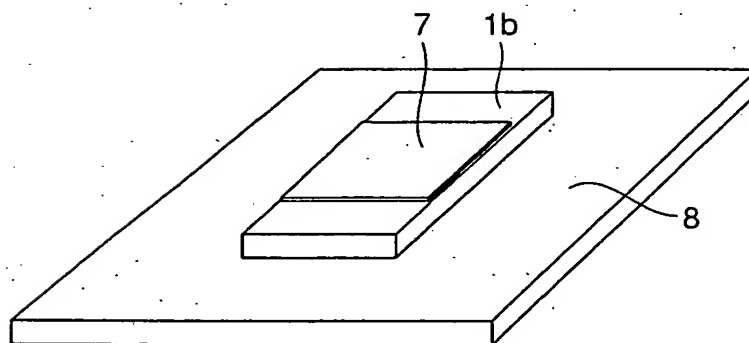


FIG. 4

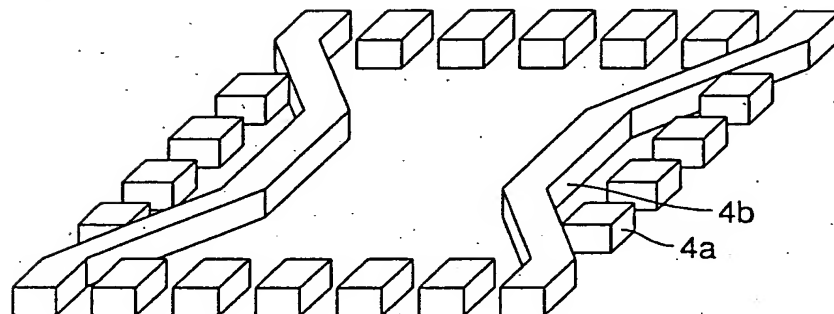


FIG. 5

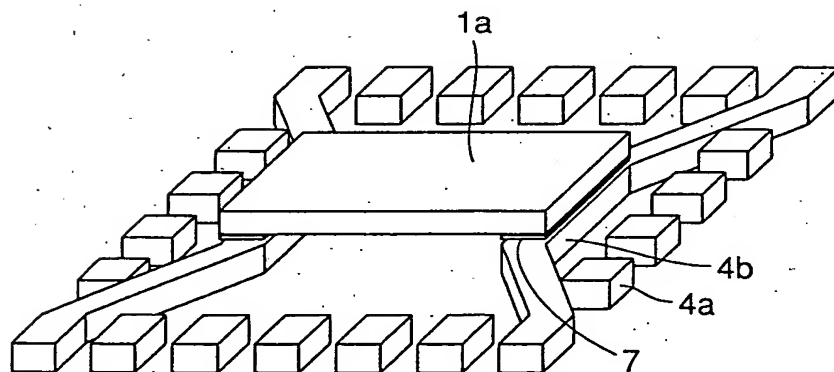


FIG. 6

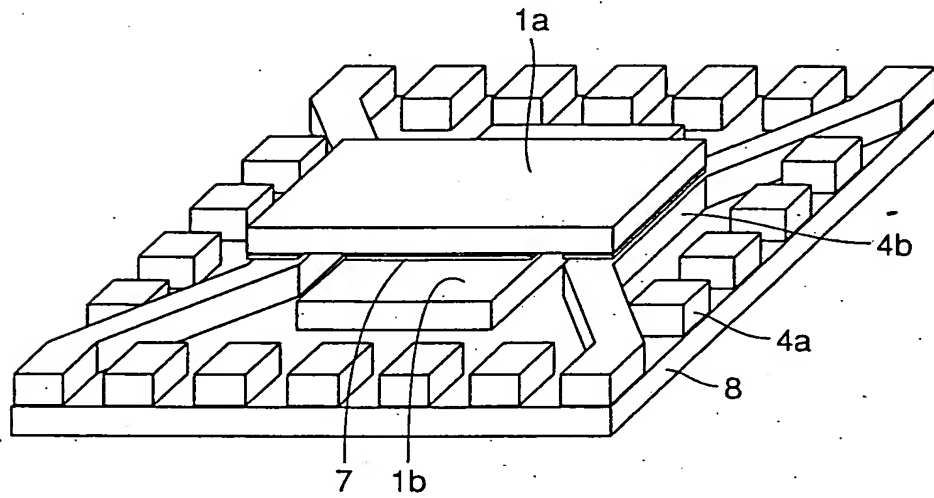


FIG. 7

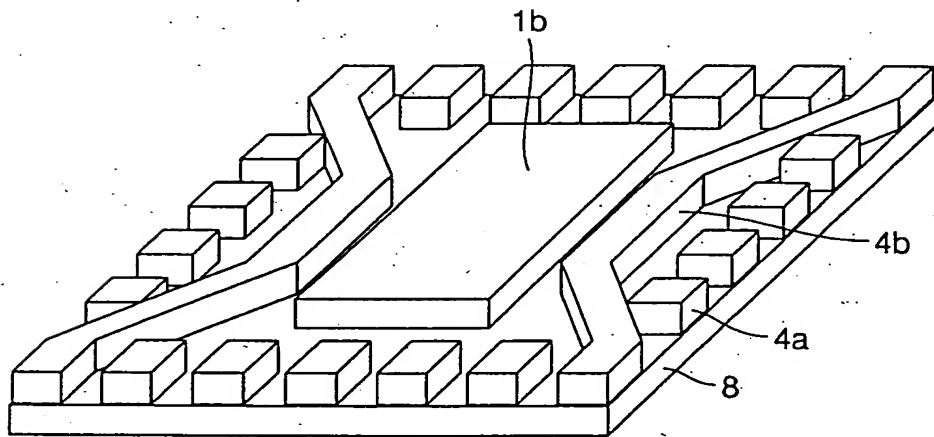
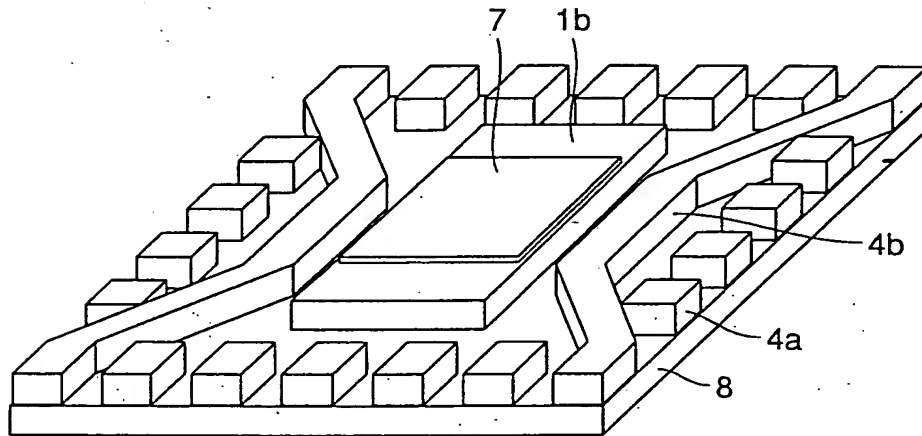


FIG. 8



Top view of a semiconductor device 100. The device features a central square region 1a. Surrounding this central region are four sets of parallel lines: 1b on the left, 4a on the right, 4b on the top, and 4 on the bottom. The device is bounded by a rectangular frame 12. Various labels indicate specific features: A and B on the left, C and D on the top, and Lm near the bottom-left corner. Arrows indicate directions or connections.

FIG. 10

A-A'

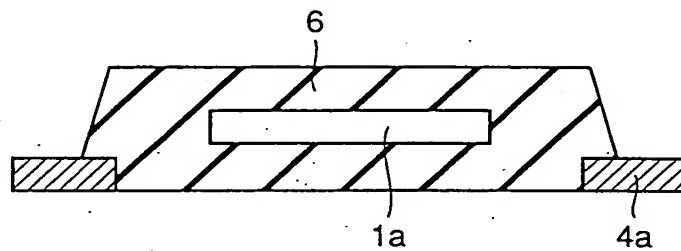


FIG. 11

B-B'

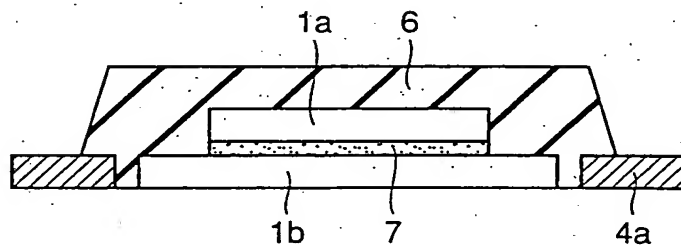


FIG. 12

C-C'

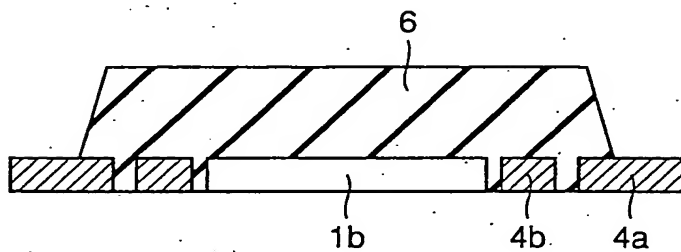




FIG. 13

D-D'

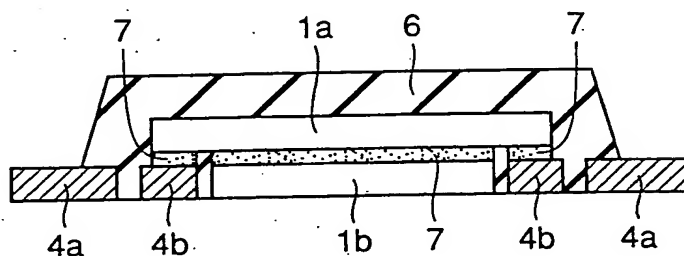


FIG. 14

A-A'

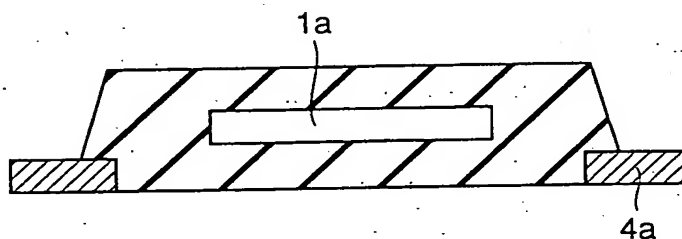


FIG. 15

B-B'

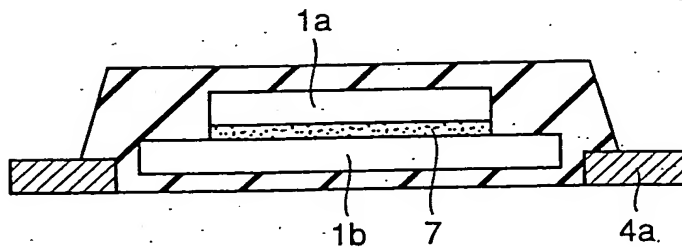


FIG. 16

C-C'

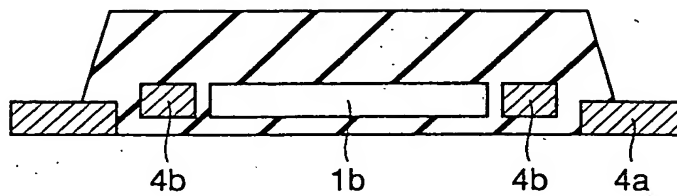


FIG. 17

D-D'

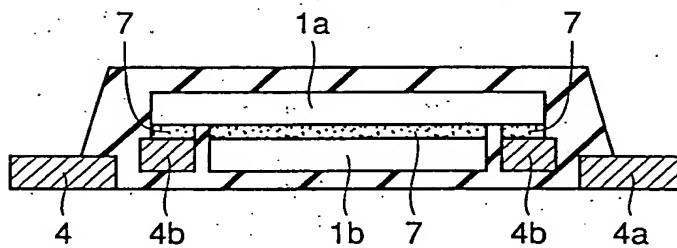


FIG. 18

A-A'

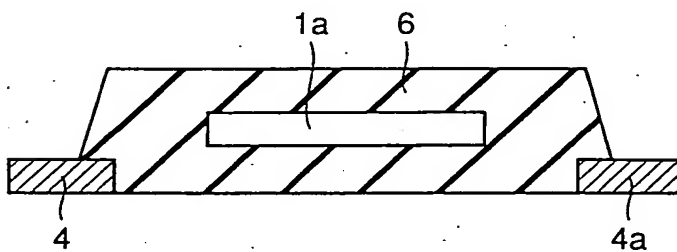


FIG. 19

B-B'

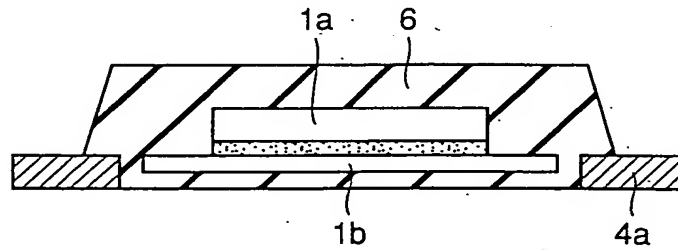


FIG. 20

C-C'

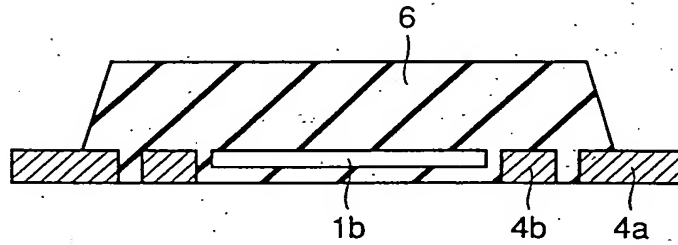
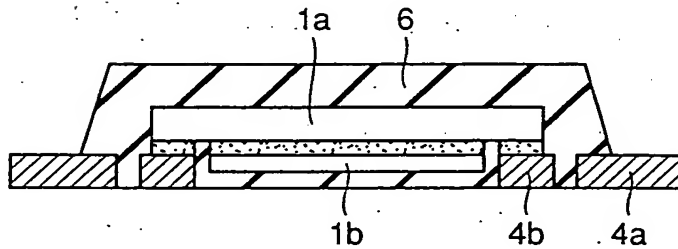


FIG. 21

D-D'



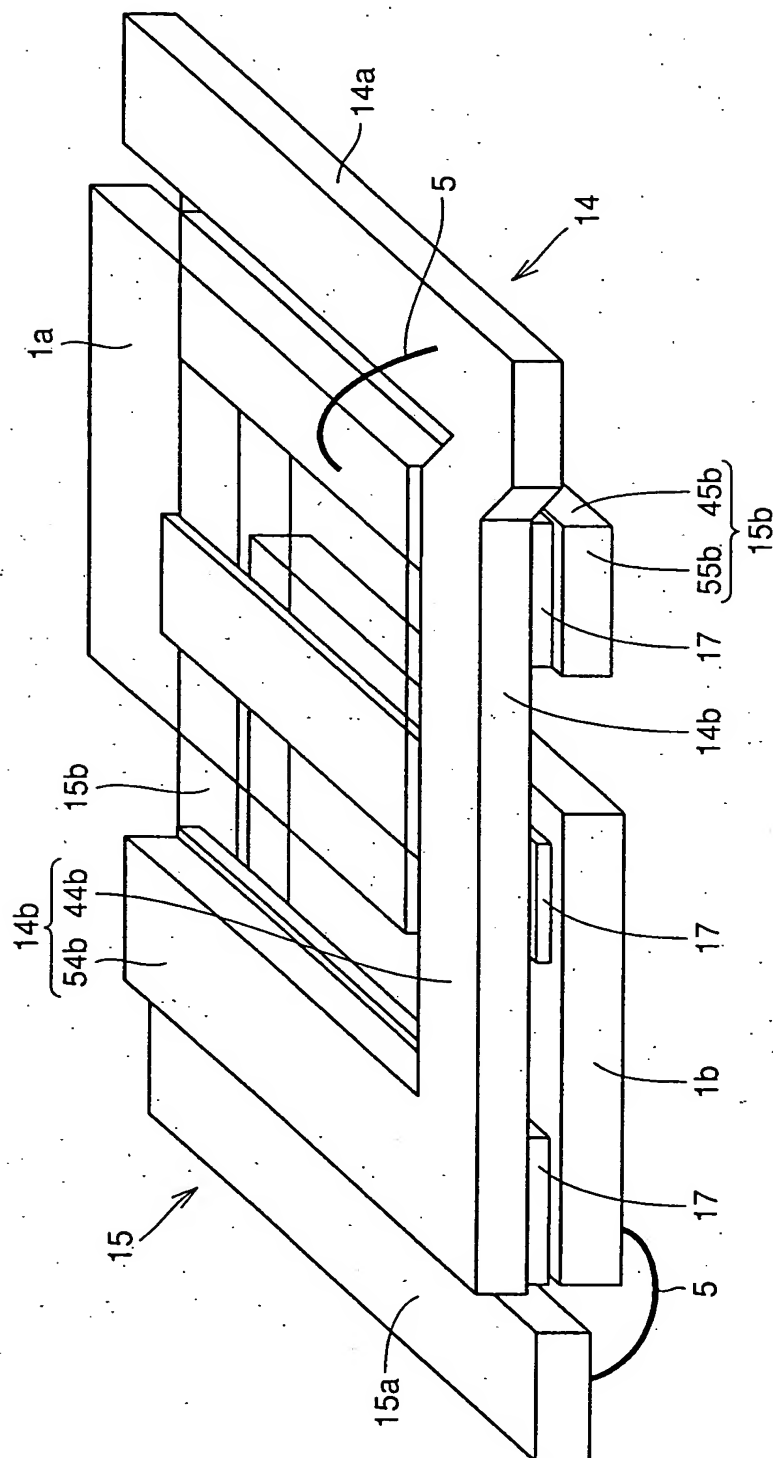


FIG. 22

FIG. 23

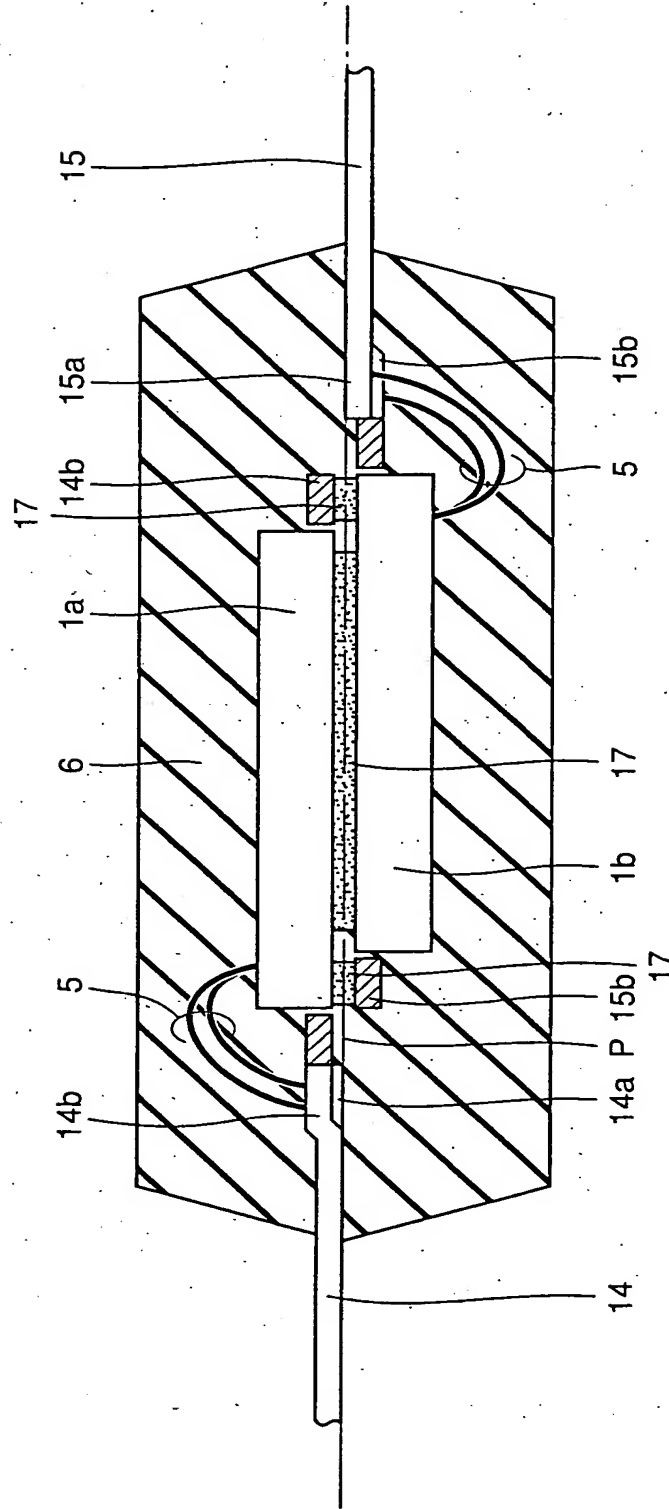


FIG. 24

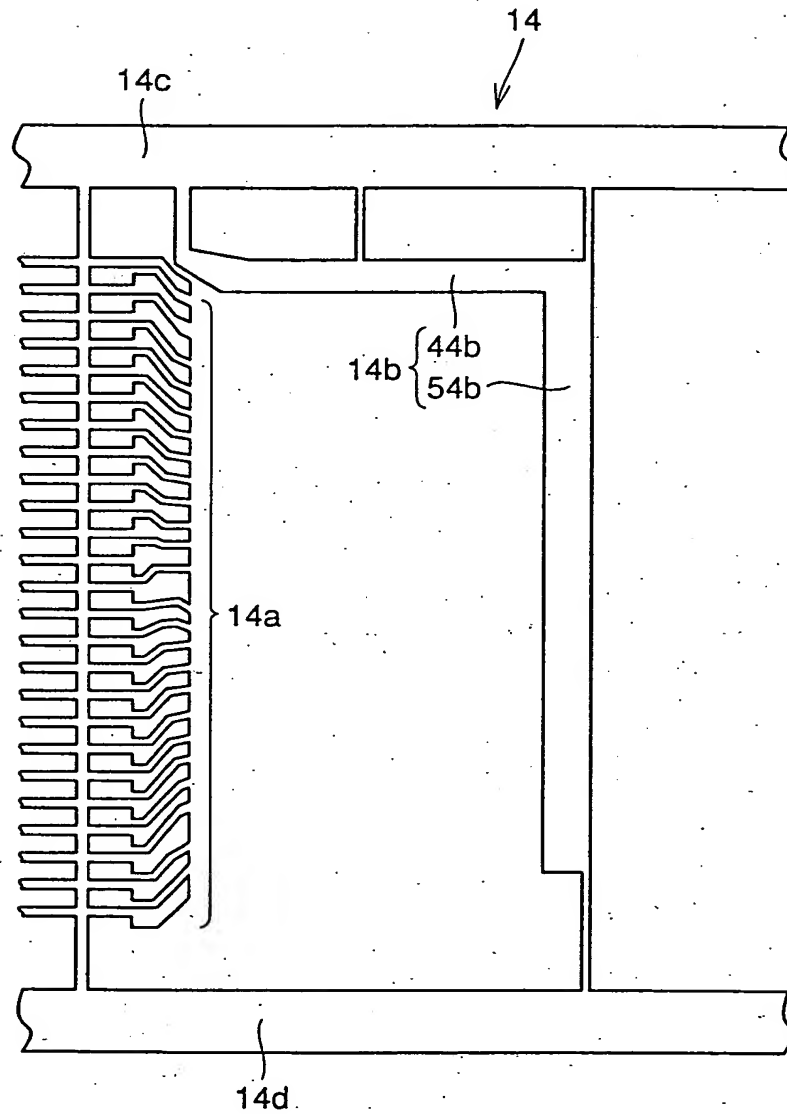


FIG. 25

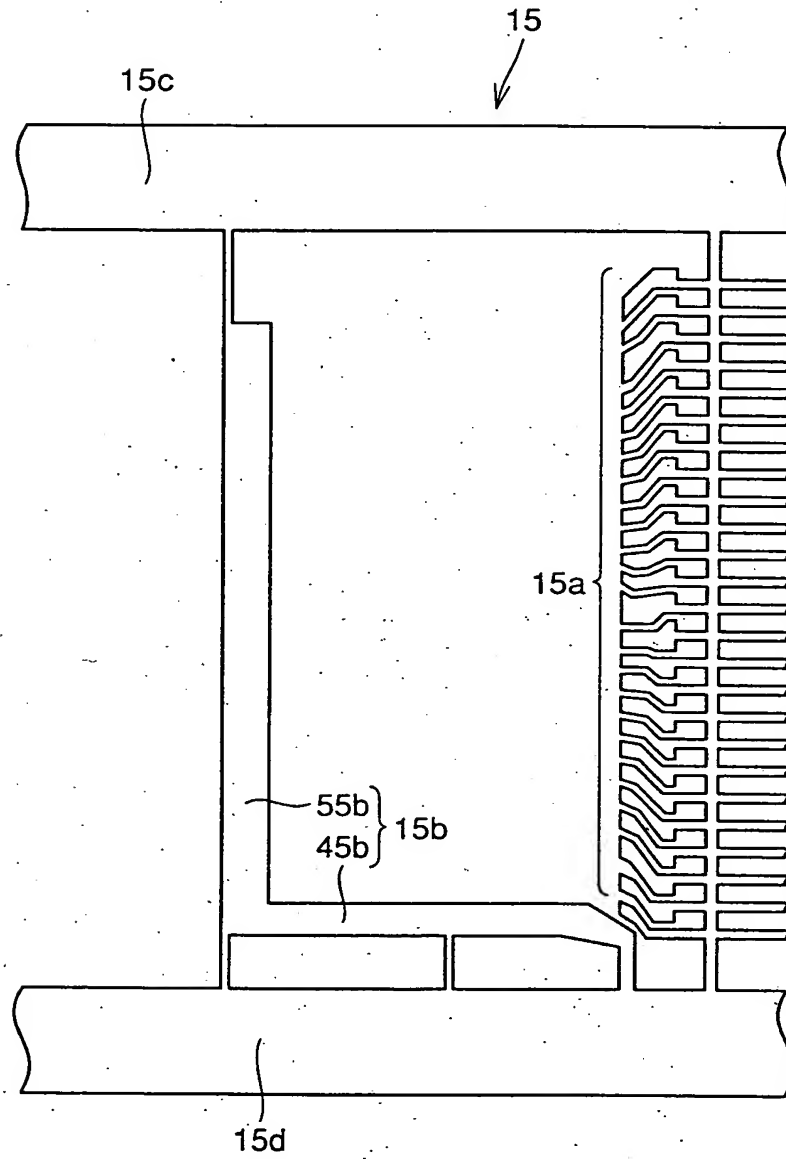


FIG. 26

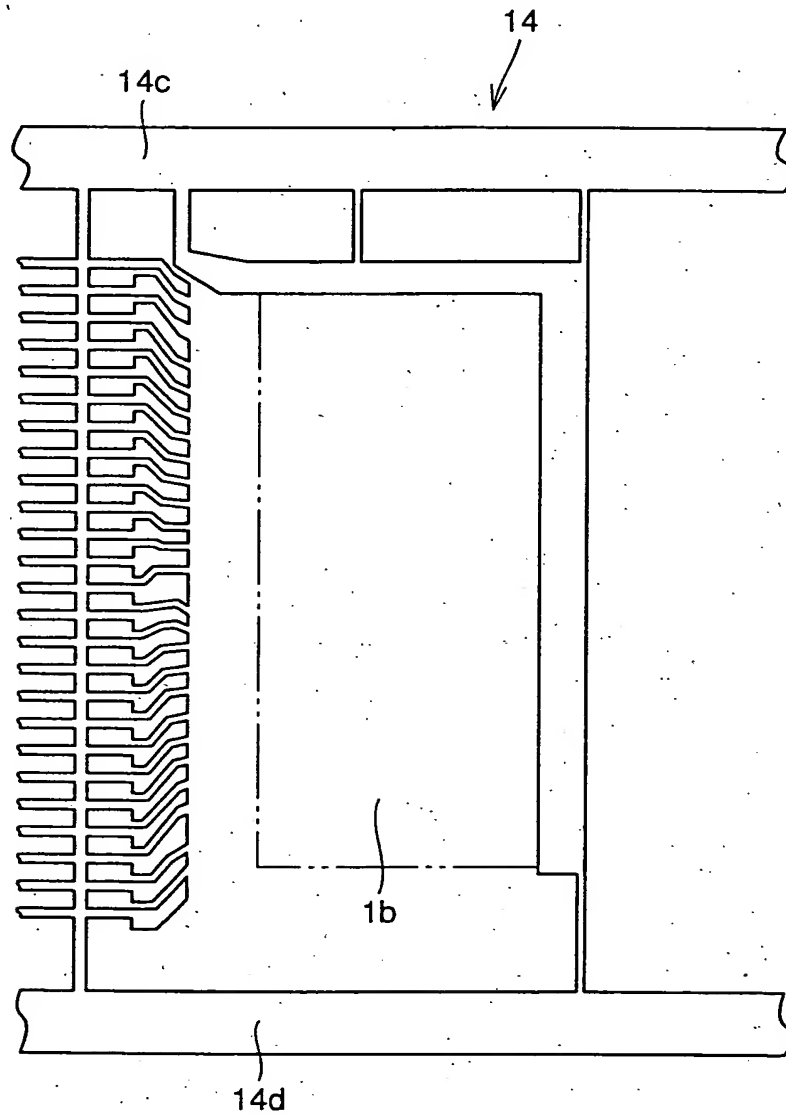




FIG. 27

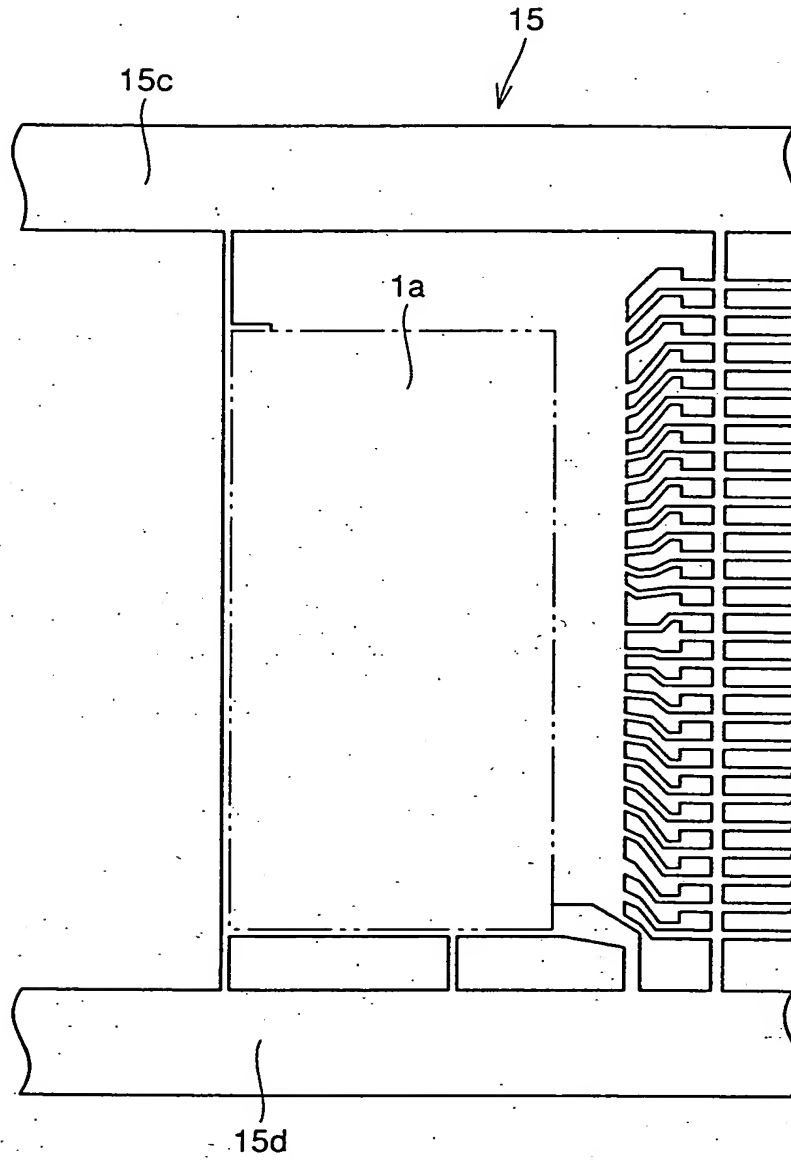


FIG. 28

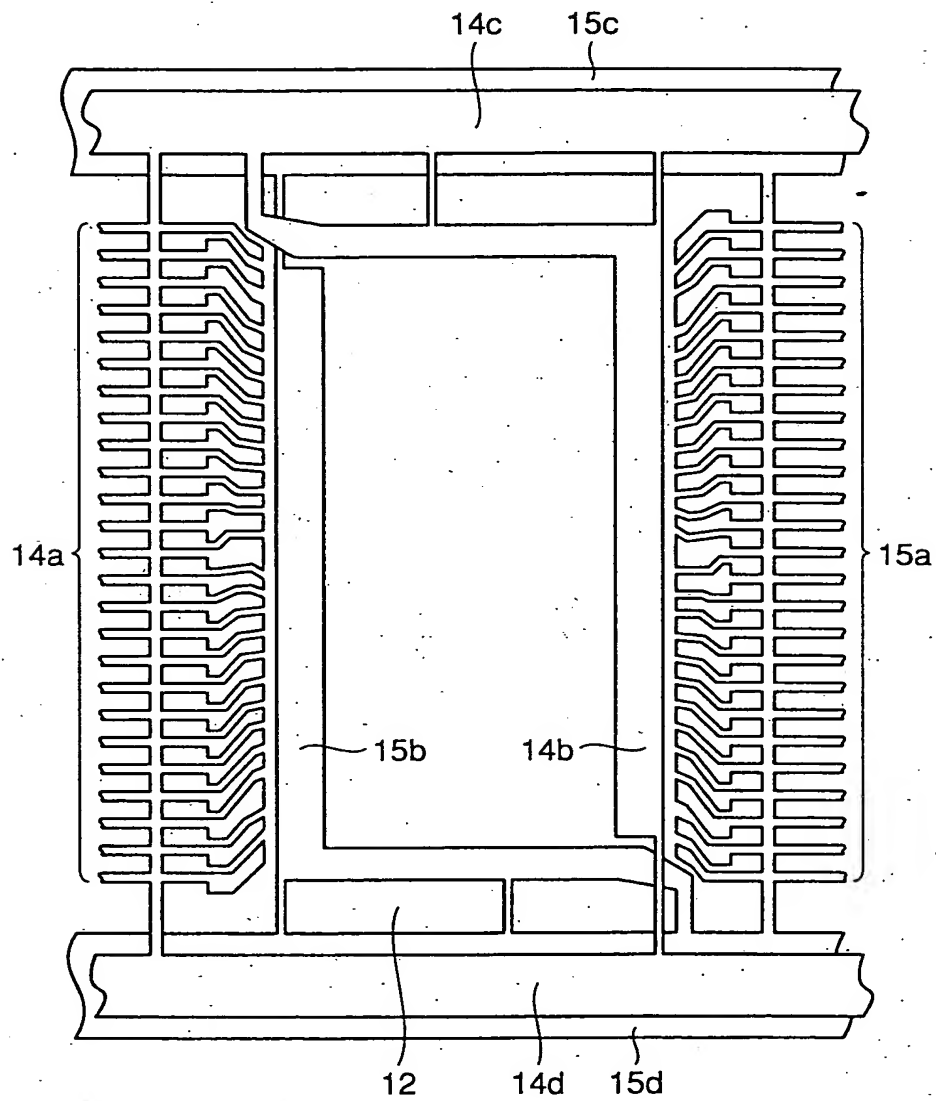


FIG. 29

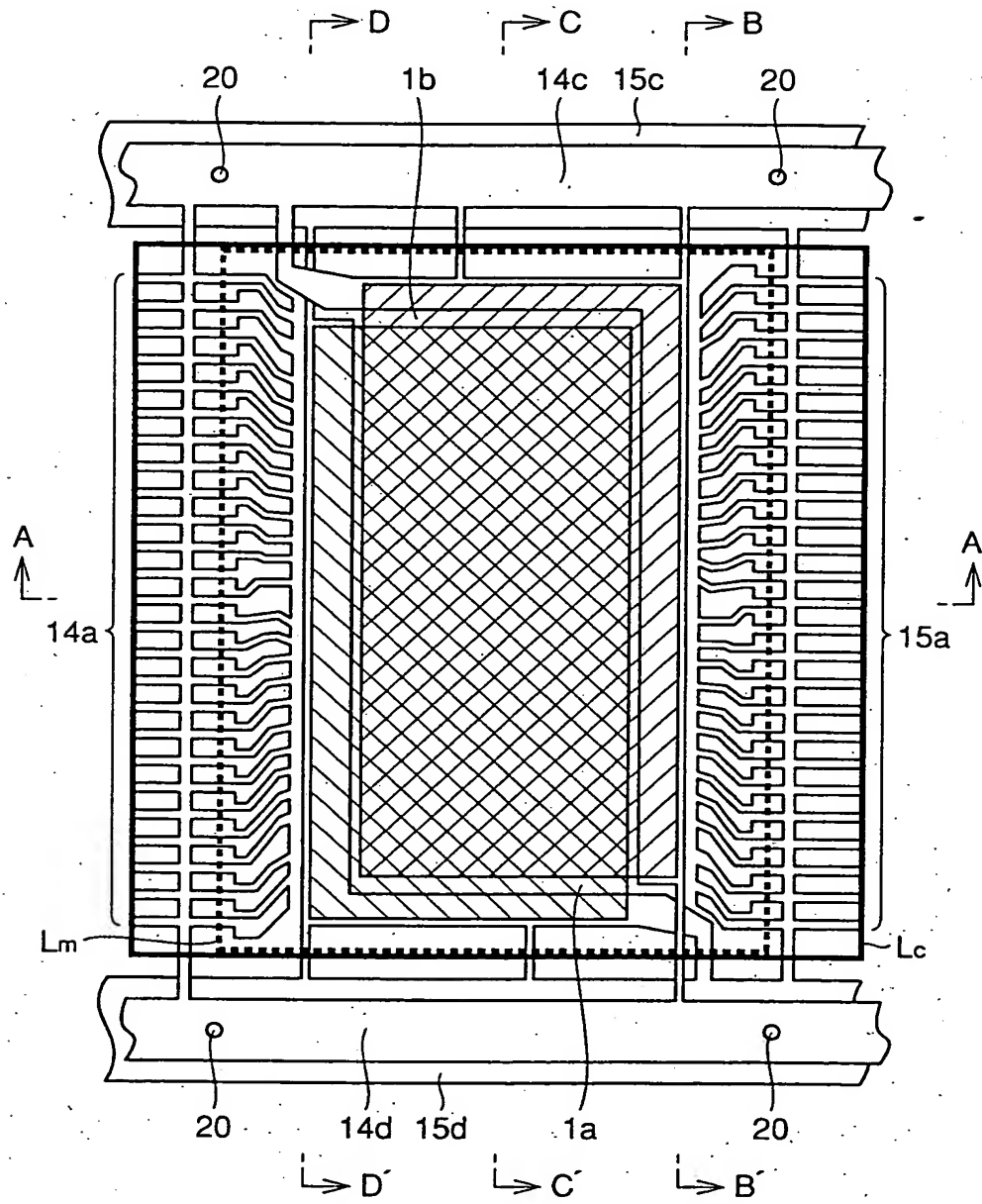


FIG. 30

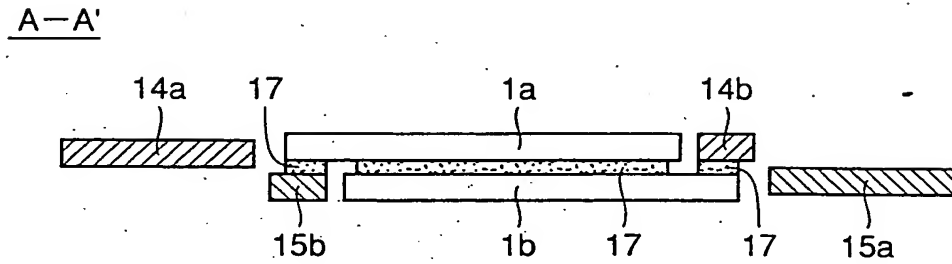


FIG. 31

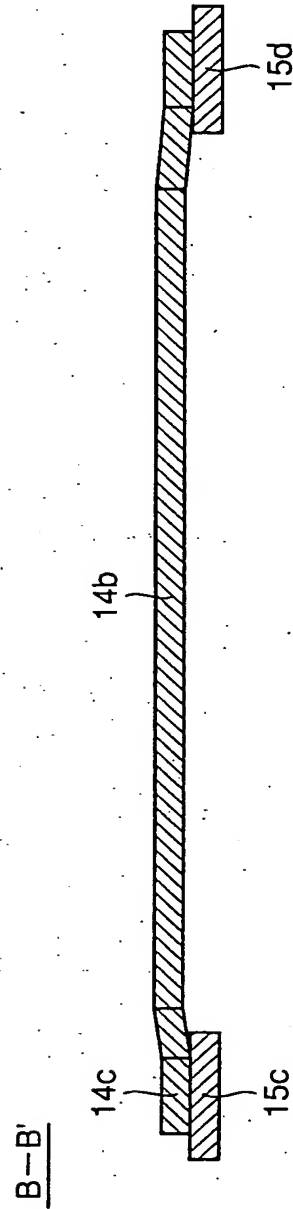


FIG. 32

C-C'

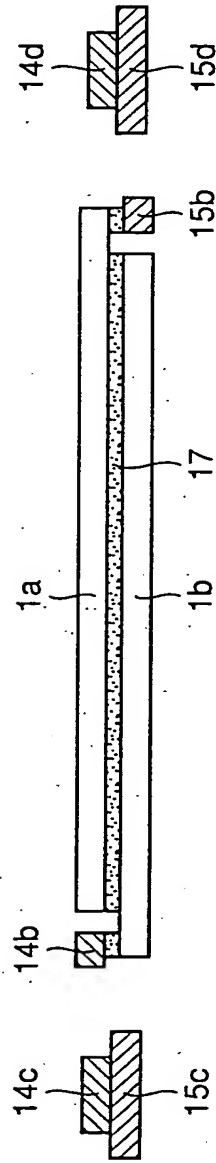


FIG. 33

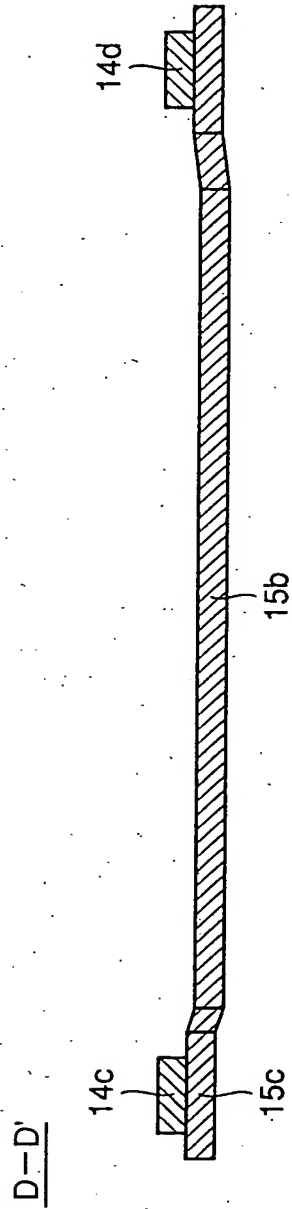


FIG. 34

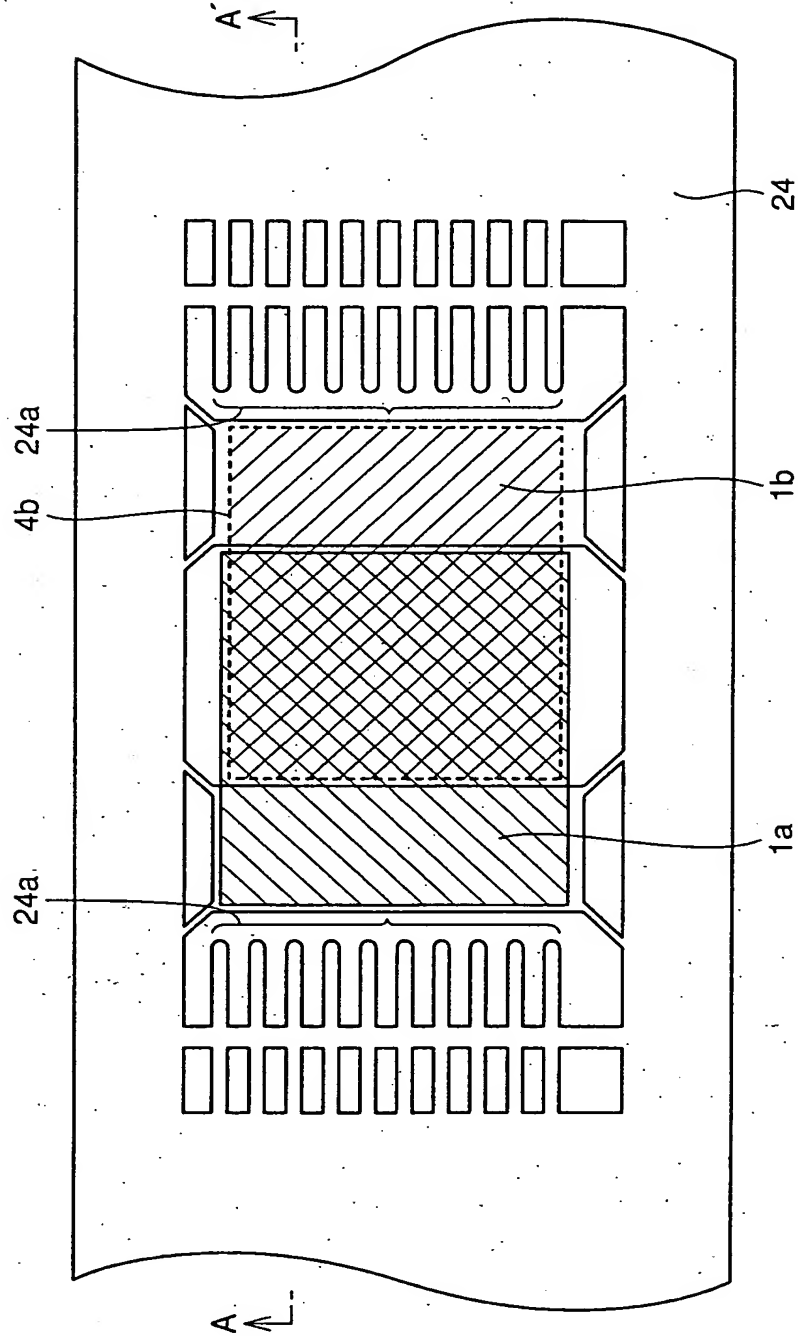




FIG. 35

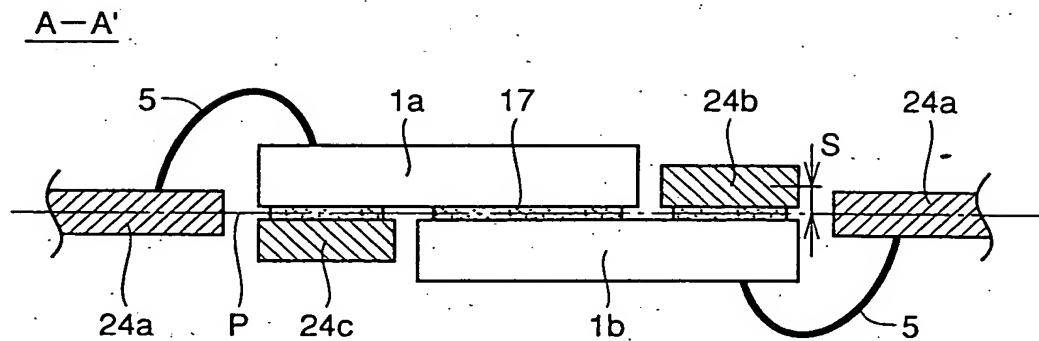


FIG. 36 PRIOR ART

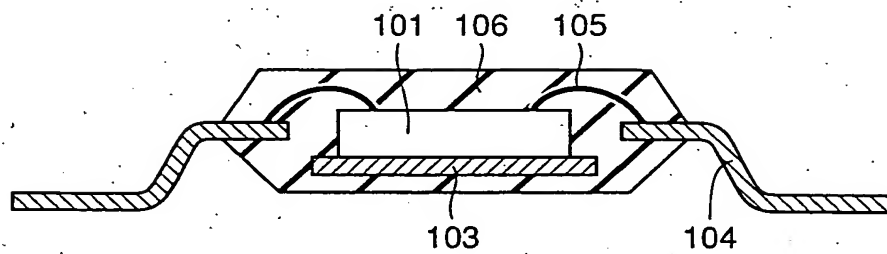


FIG. 37 PRIOR ART

